

WSG303S Sigfox Verified Module (RC1)

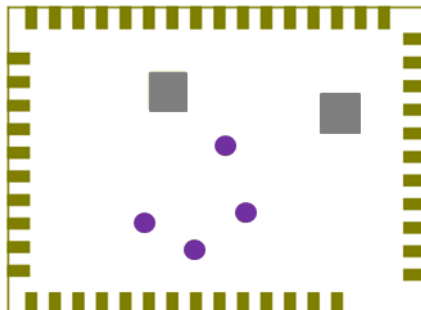
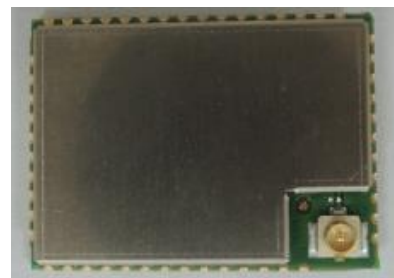
RF Output Power Up To 14dBm (sigfox)

WSG303S RC1 is a Sigfox modem module for the low power wide area network (LPWAN) market. It is designed with STM's system S2-LP+STM32 MCU for the European market (EN 300 220). The module was designed for high performance, high quality, low cost, small form factor and most importantly, high RF power of up to 14dBm. The design is fully compliant to ETSI regulations. The Sigfox application is running on SMT32 MCU at high efficiency executed at high efficiency using its internal 32bit core Cortex-M0 processor. Every module is preloaded with Sigfox application software, module specific ID/KEY/PAC as referring to Sigfox network system. The preloaded software also includes a bootloader which allows software update or future user application development.

(Pin Pin to pin compatible with WSG300S RC2/4 Module)

Features

- Operating Frequency: ISM 868MHz
- Sigfox compliant AT command set via UART
- Maximum transmission power: 14dBm
- Current consumption:
 - 26mA Tx at 14dBm (peak current with Sigfox packet transmission)
 - 4.7uA at sleep mode
- Small-form-factor
 - 22.5x16.5mm Stamp type
 - Compact board design with low external component counts
- Voltage supply : 2.0V ~ 3.6V
- Operation Temperature: -40°C ~ 85°C
- 64KB Flash & 8KB RAM Embedded
- ETSI EN 300 220 compliant
- Preloaded Sigfox application with ID/KEY/PAC and bootloader for firmware update
- Evaluation kit available



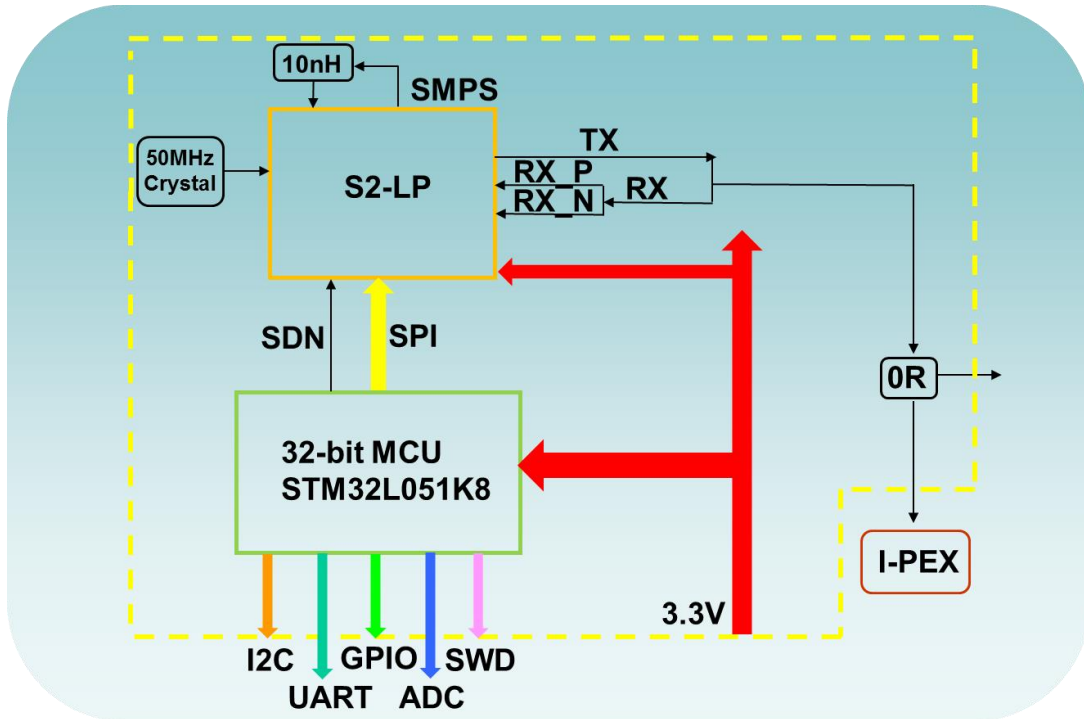
Pinout (Top view)

- Thermal pad
- Test pad

Pin number	Description	Function
1,2,3,4,5,15,16,39,40,41,42,43,44,45,47,48,49,50,51	GND	GND
6	SDO	PA6
7	SDI	PA7
8	SCLK	PB3
9	CSN	PA1
10	GPIO0	PA0
11	GPIO1	PA4
12	GPIO2	PB1
13	GPIO3	PA15
14	SDN	SDN=0, Shutdown mode
17	USART2_TX	PA2 (115200bps)
18	USART2_RX	PA3 (115200bps)
19	PA5	PA5
20	PB0	PB0
21	NC	NC
22	UART1_TX	PA9 (115200bps)
23	UART1_RX	PA10 (115200bps)
24	PA11	PA11
25	PA12	PA12
26	SWDIO	PA13 (MCU debugging and programming)
27	SWCLK	PA14 (MCU debugging and programming)
28	PA15	PA15
29	PB4	PB4
30	PB5	PB5
31	I2C1_SCL	PB6
32	I2C1_SDA	PB7
33	NRST	NRST (MCU Reset)
34	BOOT0	BOOT0 (Floating or Low)
35	PB8	PB8
36	XI	PC14-OSC_IN (Ready for External Crystal)
37	XO	PC15-OSC32_OUT (Ready for External Crystal)
38	VDD	VDD_3V3
46	ANT1	SigFox Antenna

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Functional Block Diagram



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